MODEL NAME: CV700U2-L01

Version.01

☐ **Tentative Spec.**
☐ **Preliminary Spec.**
■ **Final Spec.**

Customer's Approve	**CHOT**
Signature： Date：	Approved by Project Director Signature： Reviewed by RD manager Signature： Date： Prepared by PM Signature： Date：

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Revised Record				
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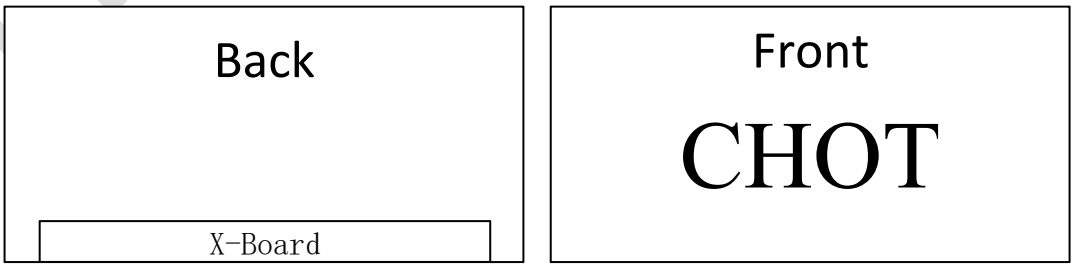
1. General Description

1.1 Overview

The CV700U2-L01 is a color active matrix LCD open cell that uses amorphous silicon TFT as a switching device. This open cell is composed of a color TFT-LCD panel, driver IC, PCBA etc., but does not include the backlight &T-con unit. Graphics and texts can be displayed on a 3840×RGB×2160 dots panel with about 16.8M colors (R/G/B 8bit in each color) by using USI-T (12-lane) interface. The components comply with ROHS for identification.

1.2 Product Features

CHARACTERISTICSITEMS	SPECIFICATIONS	UNIT
Screen Diagonal	69.5	inch
Active Area	1549.44(H) x 846.72(V)	mm
Physical Size	1561.44 (W) x 859.22 (H) x 1.365 (D)	mm
Number of Pixels	3840（H）× 2160（V）	pixel
Sub-Pixel Pitch	0.4035(H) x 0.392(V)	mm
Pixel Arrangement	RGB Vertical Stripe	-
Glass thickness (Array / CF)	0.5 / 0.5	mm
Weight	3859 Typ.	g
Display Mode	Normally Black	-
Contrast Ratio	(5000:1) Typ. (Typical value measure by CHOT's Module)	
Color Chromaticity	R = (0.653,0.321) G = (0.271,0.579) B = (0.136,0.096) W= (0.295,0.339) * Please Refer to "Color Chromaticity" in 6.2	-
Cell Transparency [%]	(4.9%) Typ. *Please refer to "Transmittance" in 6.2	-
Viewing Angle (CR>10)	Typ. +89/-89(H), +89/-89(V) (CR≧10) (Typical value measured by CHOT's Module)	-
Polarizer Surface Treatment	AG (Haze2.4±2%) Hardness: 2H	-
Rotation Function	Unachievable	-
Display Orientation	Signal input with "CHOT"	-



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2. Absolute Maximum Rating

2.1 Absolute Maximum Rating of Environment

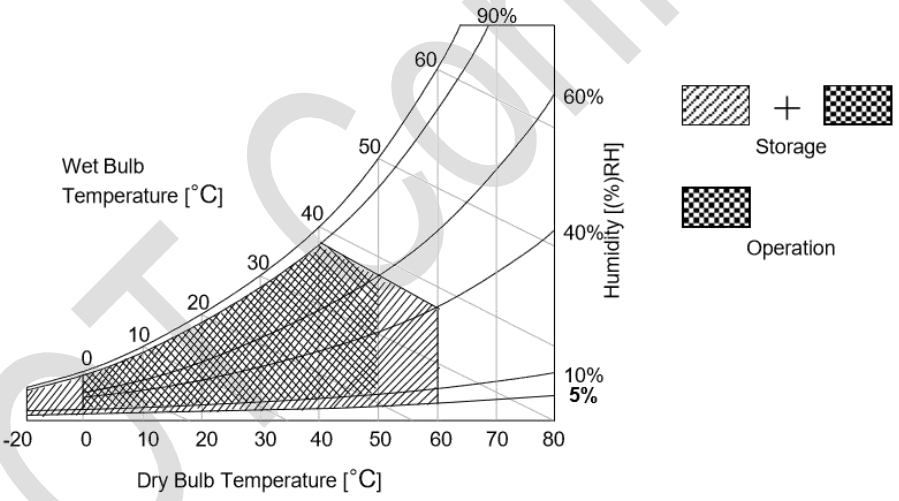
The followings are maximum values which, if exceeded, may cause faulty operation or damage to the unit or the unrecoverable damage on the product.

Item	Min	Max	Unit	Note
Input Signal Voltage	-0.3	+3.6	V	1
Operating Temperature	0	+50	℃	2
Storage Temperature	-20	+60	℃	2
Operating Humidity	10	90	%RH	2
Storage Humidity	10	90	%RH	2
Panel Surface Temperature	-	65	℃	3
Life time	30000		Hr	Only For Black Frog Mura

Note (1) Ambient temperature condition (Ta=25±2℃)

Note (2) Maximum wet-bulb should be 39℃ and no condensation. The relative humidity must not exceed 90% non-condensing at 40℃ or less. At temperature greater than 40℃, the welt bulb temperature must not exceed 39℃.

Note (3) Surface temperature is measured at 50℃ dry condition.



2.2 Absolute Maximum Rating of Environment (Open Cell)

Recommended storage condition: with shipping package

Recommended storage temperature range: 25±5℃

Recommended storage humidity range: 50±10%RH

Recommended shelf life: one month

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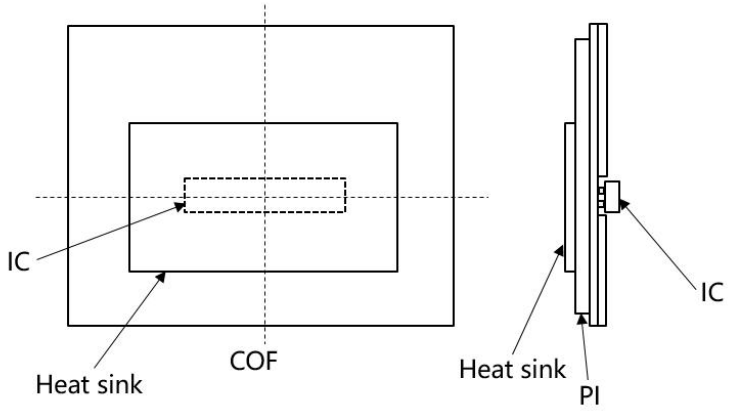
3. Electrical Specification

3.1 Power Consumption (Ta = 25 ± 2 ºC)

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power supply voltage	VAA	16.38	16.68	16.98	V	
Power supply voltage	HVAA	L_H+0.2	-	U_L-0.2	V	
Gamma voltage	U_H	15.52	15.72	1592	V	
	U_L	8.54	8.69	7.84		
	L_H	7.05	7.2	7.35		
	L_L	0.46	0.56	0.66	V	
Digital power supply for source driver	VCCA1.8V	1.62	1.8	1.98	V	
Digital power supply for source driver	VCCA1.9V	1.72	1.9	2.08	V	
Power supply for gate off output	VSS1	-8.3	-8	-7.7	V	
Power supply for gate off output	VSS2	-10.3	-10	-9.7	V	
Power supply for common electrode	VCOM1	6.2	6.35	6.5	V	
Power supply for common electrode	VCOM2	7.66	7.81	7.96	V	
Digital power supply	VDD33	3.0	3.3	3.6	V	



Device	Max. of surface temperature	Unit	Note
Source driver	125	℃	1

Note (1) Any point on the IC surface must be less than the Max. of Surface Temperature under any conditions. If the surface temperature is out of the spec, thermal solutions should be applied to avoid the damage;

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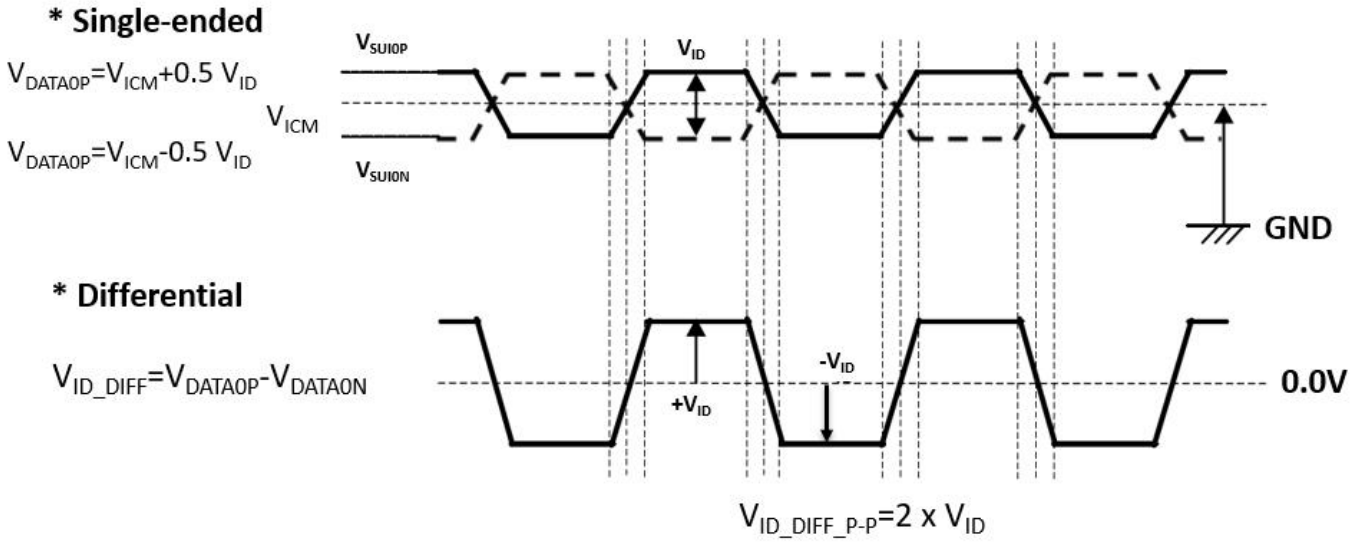
3.2 USIT Characteristic

Parameter	Symbol	Condition	Value			Unit	Note
			Min.	Typ.	Max.		
Input offset voltage	VICM	VDD1A = 1.62 V to 1.98 V	0.3	0.45	0.6	V	(1)
Differential input data voltage	V_{ID}		100	-	600	mV	(2) (3)
Differential input data peak-to-peak voltage	$V_{ID_DIFF_P-P}$		200	-	1200	mV	(4)
Internal termination resistor	R_T	TC(1)	Typ-20%	110	Typ+20%	Ω	(5)

Note (1) VICM voltage is the common mode voltage of the differential input data.
Note (2) The min/max level of VID includes all the AC fluctuation upon DC level.
Note (3) The min level of VID means internal min value of USI-T data and the max level of VID means external max value of the USI-T data in eye diagram, respectively.
Note (4) The conditions of measurement for eye diagram are to remove the driver IC and to attach 100 Ω termination resistor on the source PCB, and to bond a Driver IC on the source PCB, respectively.
Note (5) R_T is an internal termination resistor (On-Die Termination).



Condition is $T_{C.typ}$ (52.5 ℃)

4. Interface Electrical Specification

4.1 Pin Assignment

Matting connector:(05002HR-H96VE）or equivalent. (note1)

Pin No.	Symbol	Description	Note
1	AVDD	Analog power supply	
2	AVDD	Analog power supply	
3	AVDD	Analog power supply	
4	AVDD	Analog power supply	
5	AVDD	Analog power supply	
6	AVDD	Analog power supply	
7	AVDD	Analog power supply	
8	AVDD	Analog power supply	
9	U_H	Gamma voltage	
10	U_L	Gamma voltage	
11	HAVDD	Analog power supply	
12	HAVDD	Analog power supply	
13	L_H	Gamma voltage	
14	L_L	Gamma voltage	
15	FB_2	Connect with FB_1	
16	GND	Ground	
17	SFC1	Shared forward channel 1	
18	GND	Ground	
19	CH5_B-	USI-T12-	
20	CH5_B+	USI-T12+	
21	GND	Ground	
22	CH5_A-	USI-T11-	
23	CH5_A+	USI-T11+	
24	GND	Ground	
25	CH4_B-	USI-T10-	
26	CH4_B+	USI-T10+	
27	GND	Ground	
28	CH4_A-	USI-T9-	
29	CH4_A+	USI-T9+	
30	GND	Ground	
31	CH3_B-	USI-T8-	
32	CH3_B+	USI-T8+	
33	GND	Ground	
34	CH3_A-	USI-T7-	
35	CH3_A+	USI-T7+	
36	GND	Ground	
37	CH2_B-	USI-T6-	

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Pin No.	Symbol	Description	Note
38	CH2_B+	USI-T6+	
39	GND	Ground	
40	CH2_A-	USI-T5-	
41	CH2_A+	USI-T5+	
42	GND	Ground	
43	CH1_B-	USI-T4-	
44	CH1_B+	USI-T4+	
45	GND	Ground	
46	CH1_A-	USI-T3-	
47	CH1_A+	USI-T3+	
48	GND	Ground	
49	CH0_B-	USI-T2-	
50	CH0_B+	USI-T2+	
51	GND	Ground	
52	CH0_A-	USI-T1-	
53	CH0_A+	USI-T1+	
54	GND	Ground	
55	SRF	Source driver ready feedback	(2)
56	NC	No connection	
57	VDD18	Digital power supply for source driver	
58	VDD18		
59	VDD19	Digital power supply for source driver	
60	VDD19		
61	HOLD	Demura flash hold input	
62	SCK	Demura flash serial clock input	
63	CS	Demura flash chip select input	
64	DI	Demura flash data input/output	
65	DO	Demura flash data input/output	
66	WP	Demura flash write protection	
67	Panel_3.3V	Panel_3.3V	
68	NC	No connection	
69	VSS1	Power supply for GOA	
70	VSS2	Power supply for GOA	
71	VSS2		
72	LC2	LC2 signal for GOA	
73	LC1	LC1 signal for GOA	
74	NC	No connection	
75	NC	No connection	
76	NC	No connection	
77	NC	No connection	
78	CK8	Clock input 8 for GOA	

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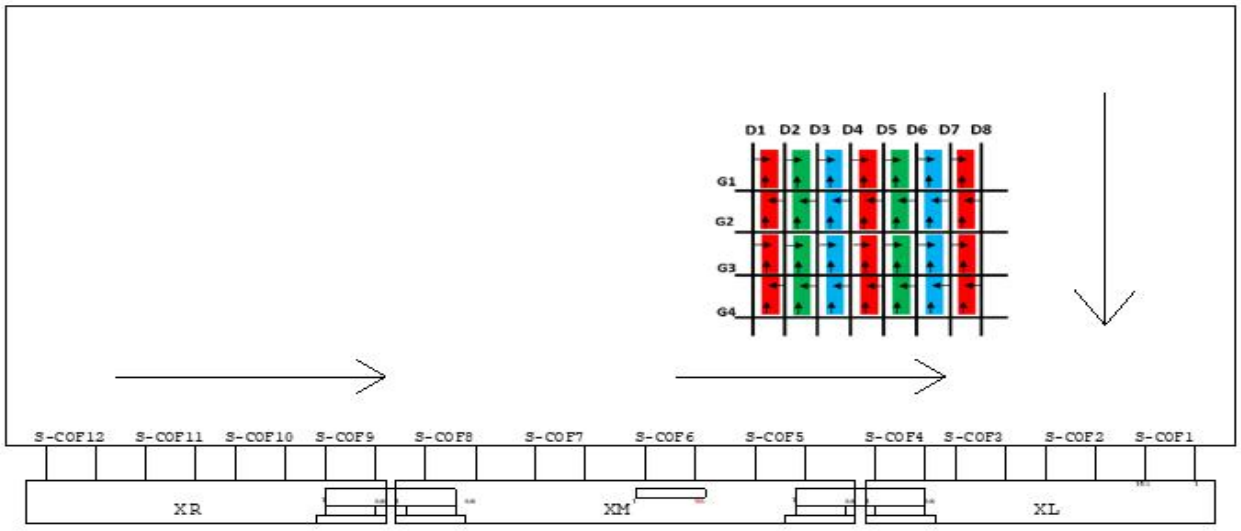
Pin No.	Symbol	Description	Note
79	CK7	Clock input 7 for GOA	
80	CK6	Clock input 6 for GOA	
81	CK5	Clock input 5 for GOA	
82	CK4	Clock input 4 for GOA	
83	CK3	Clock input 3 for GOA	
84	CK2	Clock input 2 for GOA	
85	CK1	Clock input 1 for GOA	
86	STV	Start pulse for GOA	
87	RESET	RESET for GOA	
88	NC	No connection	
89	VCOM2	Power supply for common electrode (VCOM_TFT)	
90	VCOM2		
91	VCOM1	Power supply for common electrode (VCOM_CF)	
92	VCOM1		
93	FB_VCOM2	VCOM_TFT feedback signal	
94	FB_VCOM1	VCOM_CF feedback signal	
95	GND	Ground	
96	FB_1	Connect with FB_2	



Note (1) Pin number start from the left side as the following figure.

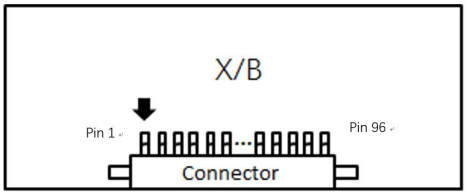
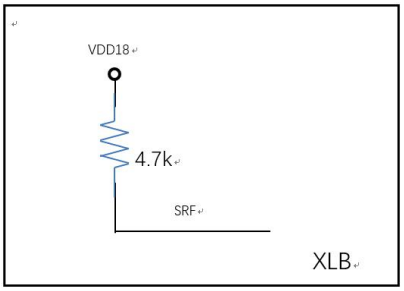


Fig4.1.1 Connector direction sketch map

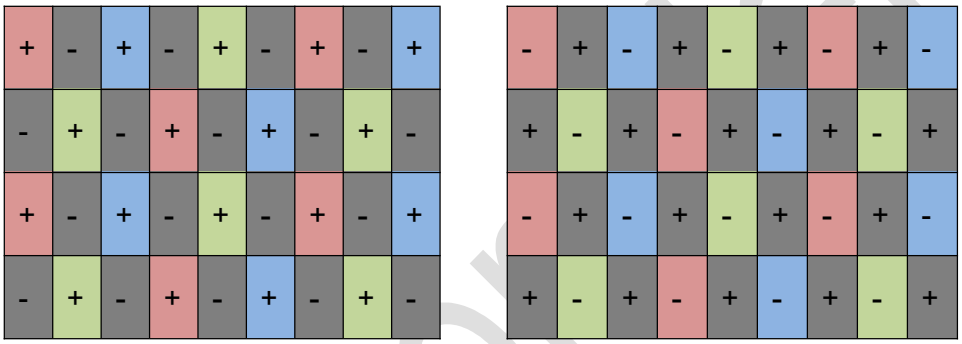
Note (2) SRF: Internal pull H with 4.7k resistor;



4.2 Flicker Pattern

Flicker Pattern shown as below.

Bright sub-pixel=G127(50% grayscale); dark sub-pixel=G0（0%grayscale）



Frame N Frame N+1

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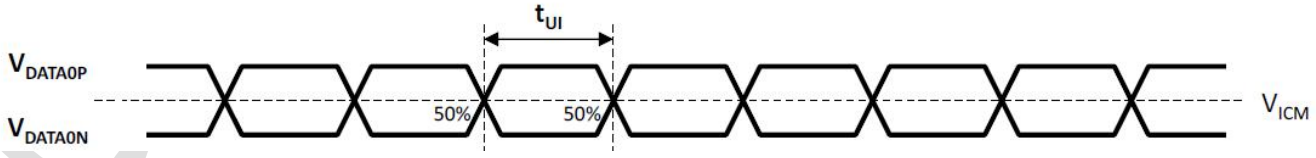
5. Interface Signal Timing

5.1 Timing Table

(VDD1(A)=1.62~1.98V, VDD2=13.5~18.5V, VSS1=VSS2=0V, Ta=-30 ~125℃)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
USI-T data frequency (For USI-T Rx)	$f_{USIDATA}$	330	-	900	MHz	1.62V ≤ VDD1(A) ≤1.98V
USI-T data pulse width	t_{UI} (1)	0.555		1.515	ns	
USI-T clock period	t_{USICLK} (1)	5.56		15.15	ns	t_{USICLK}=10 x t_{UI}
Reference clock phase locking time	t_{LOCK} (2)	4500	-	-	t_{USICLK}	
Minimum line blank period	t_{LB} (3)	21	-		t_{USICLK}	
CLK1 start time prior to the next line data	t_{CLK1} (3)	1	-		t_{USICLK}	
Power-on slope	t_0 (2)			5	ms	
Stand-by time between power- on and 1st data transfer	t_1 (2)	1	-		ms	
Training pattern offset time before and after SFC = L	t_2 (2)	5	-		t_{USICLK}	
Skew between data ports	t_{SKEW}(4)		-	250	ps	
SFC start time prior to the 1st line data	t_{START} (5)	5			H	
SFC end time after the last line data	t_{END} (5)	5			H	
USI-T SSCG	MR (Modulation Ratio)	-3		3	%	
	MR (Frequency)	27		90	KHz	
USI-T Cumulative	JIT_{ACC}	-0.1	-	0.1	UI	

Note (1) 1T = 10UI = 1 t_{USICLK}, AC timing parameters, t_{UI}.



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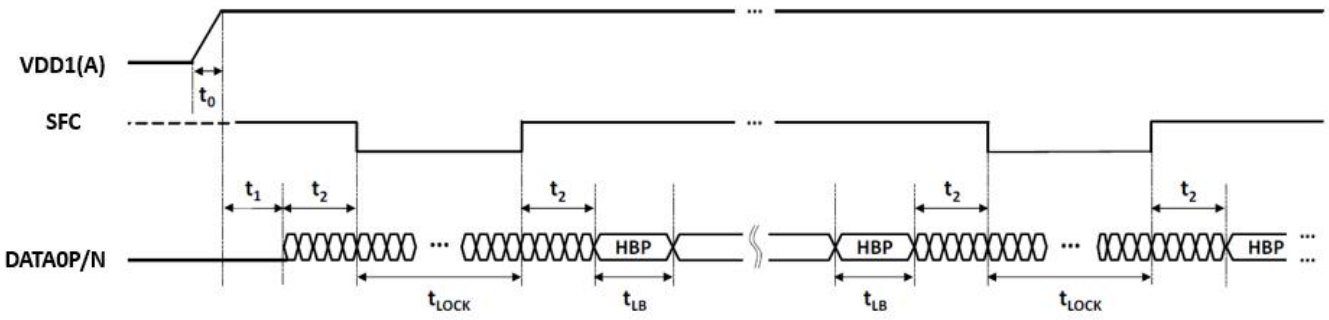
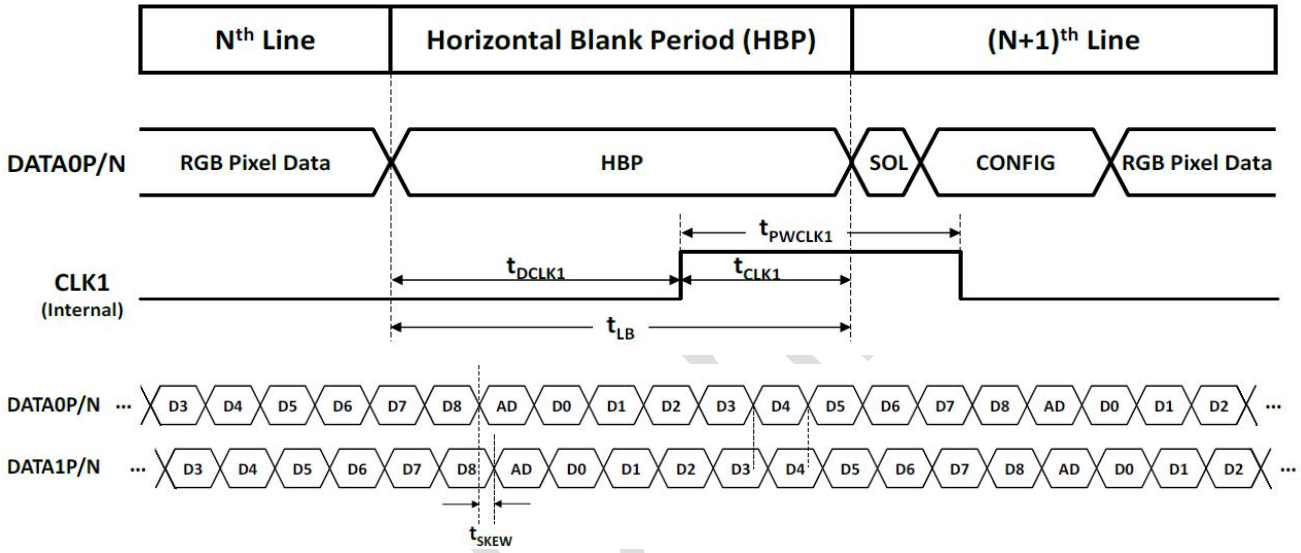
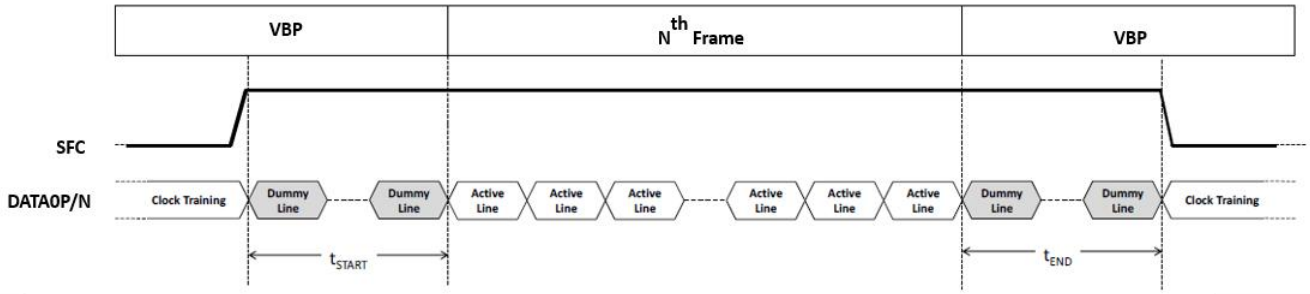
Note (2) AC timing parameters, t_0, t_1, t_2, t_{LB} and t_{LOCK}



Note (3) AC timing parameter, t_{LB}, t_{CLK1}.Last internal CLK1 should be placed in minimum 1T prior to SOL.

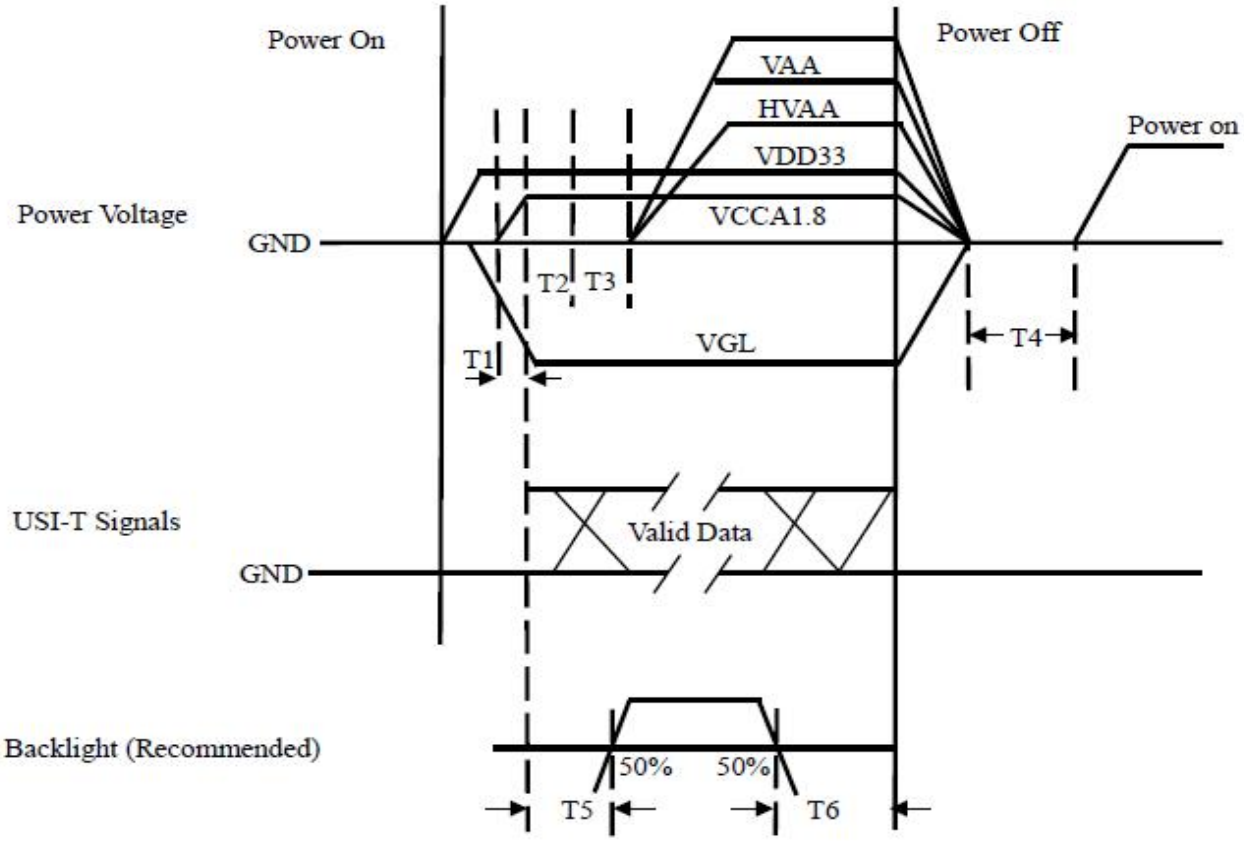


Note (4) AC timing parameter, t_{SKEW}



Note (5) These timing parameters, t_{START} and t_{END} are for the vertical blank period.

5.2 Power On/Off Sequence



Note (1) T1 is VCCA1.8V/1.9V settling time1;

Note (2) T2 is USI-T initialization period;

Note (3) T3 is Delay time from the end of USI-T initialization period to the start of VAA;

Parameter	Value			Unit
	Min.	Typ.	Max.	
T1	0		5	ms
T2	0.07		40	ms
T3	8			ms
T4	1000			ms
T5	500			ms
T6	100			ms

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5.3 Driver IC ESD

If the LCD module is designed with the Plastic Bezel, we suggest ESD protection solutions should be applied to avoid IC damaged, as shown in Fig.5.3.1.

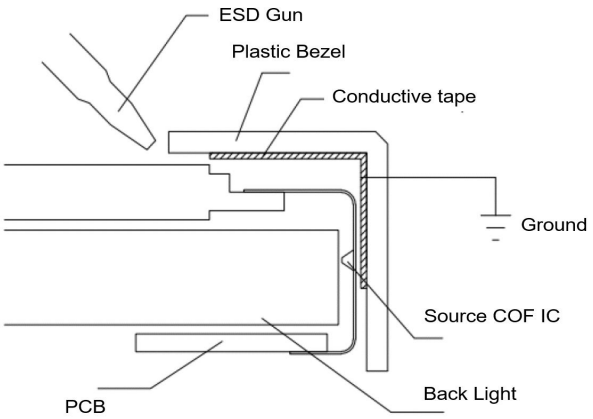

Fig.5.3.1 Source COF IC ESD protection

5.4 USIT differential signal characteristics

Item	X(UI)	Y(mV)	Note
A	0.2	0	(1)
B	0.5	75	(1)
C	0.8	0	(1)
D	0.5	-75	(1)

Table 1 Eye Mask Specification

Note(1)

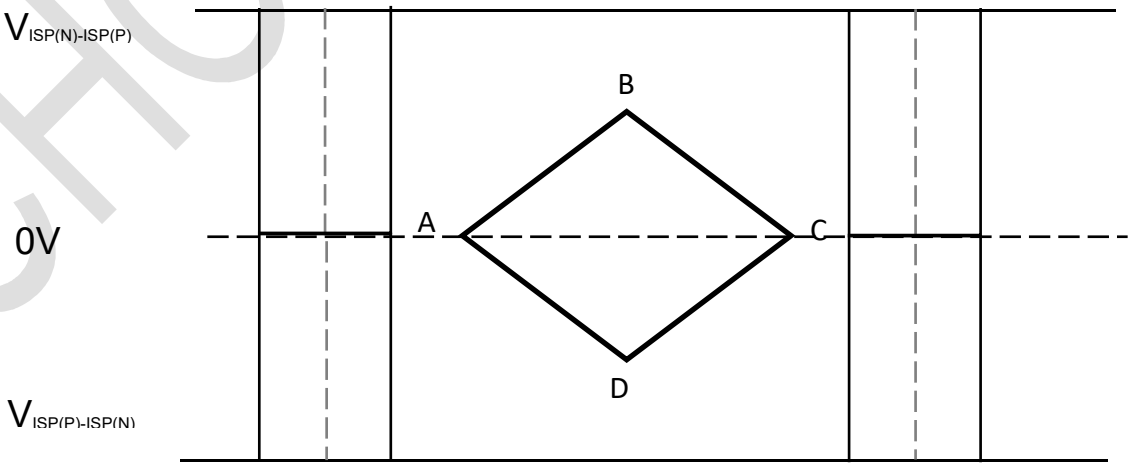

Fig5.4.1 Absolute eye-diagram mask

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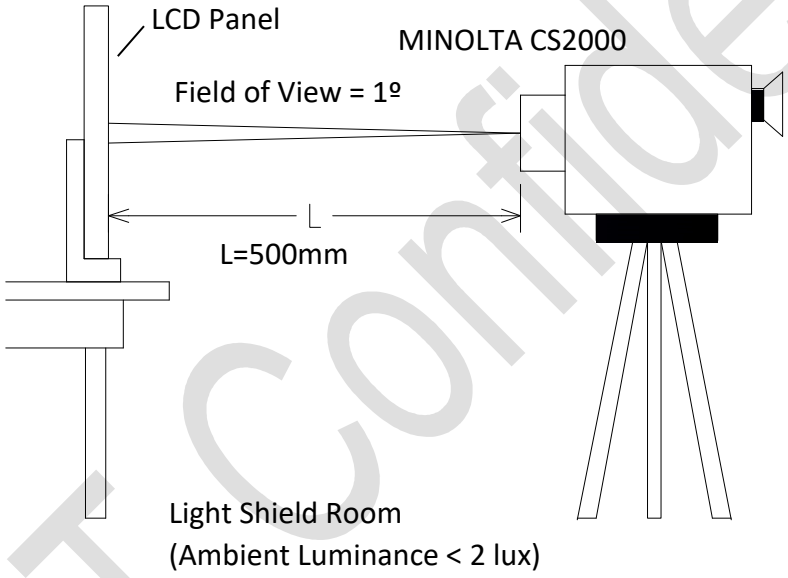
6. Optical Characteristics

6.1 Measurement Conditions

Item	Symbol	Value	Unit
Ambient Temperature	T_A	25±2	ºC
Ambient Humidity	H_A	50±10	%RH
Supply Voltage	VDD	12V±1.2	V
Vertical Frame Rate	F	60	Hz
Driving Signal	Refer to the typical value in Chapter 3: Electrical Specification		

The LCD Module should be stabilized at given temperature for 1 hour to avoid abrupt temperature change during measuring in a windless room.



6.2 Optical Specifications

The table below of optical characteristics is measured by MINOLTA CS2000 and MINOLTA CA310, in Light Shield Room.

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Color Chromaticity	Red	R_{XC}	θx=0°, θy =0° Viewing Angle at Normal Direction With Standard Light Source “C”	Typ. -0.03	0.653	Typ. +0.03	-	(1)
		R_{YC}			0.321		-	
	Green	G_{XC}			0.271		-	
		G_{YC}			0.579		-	
	Blue	B_{XC}			0.136		-	
		B_{YC}			0.096		-	
	White	W_{XC}			0.295		-	
		W_{YC}			0.339		-	

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Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	θx=0°, θy =0° With CHOT BLU	3500	5000	-	-	(2)(4)
Response Time		Gray to gray		-	8	16	ms	(2)(5)
Center Transmittance		T%		4.4	4.9		%	(6)
Gamma		-		1.9	2.2	2.5	-	
Crosstalk		CT		-	-	2	%	(7)
Viewing Angle	Horizontal	θ_{x+}	CR≥ 10 With CHOT BLU	80	89	-	Deg.	(2)(3)
		θ_{x-}		80	89	-		
	Vertical	θ_{y+}		80	89	-		
		θ_{y-}		80	89	-		

Note (1) Light source is the standard light source ”C” which is defined by CIE 1931 and driving voltage are based on suitable gamma voltages. The calculating method is as following: 1. Measure Module’s and BLU’s spectrum at center point. W, R, G, B, are with signal input. BLU is supplied by CHOT. 2. Calculate cell’s spectrum. 3.Calculate cell’s chromaticity by using the spectrum of standard light source “C”.

Note (2) Light source is the BLU which supplied by CHOT and the cell driving voltage are based on suitable gamma voltages.

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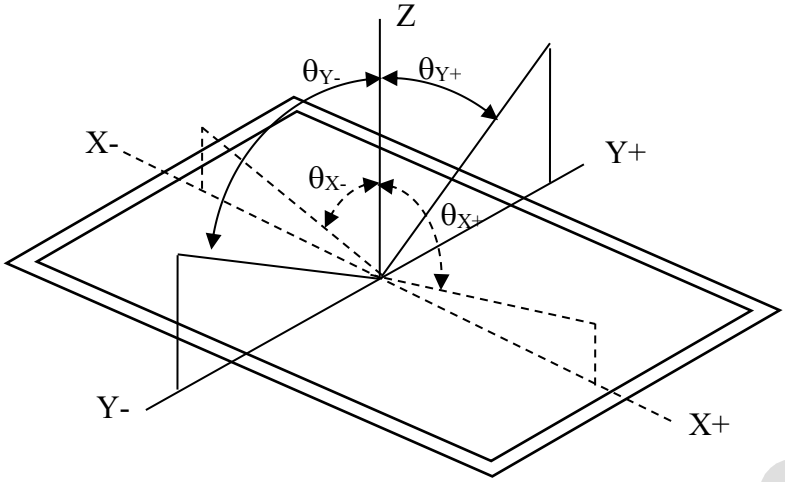
Note (3) Definition of Viewing Angle (θx,θy) :

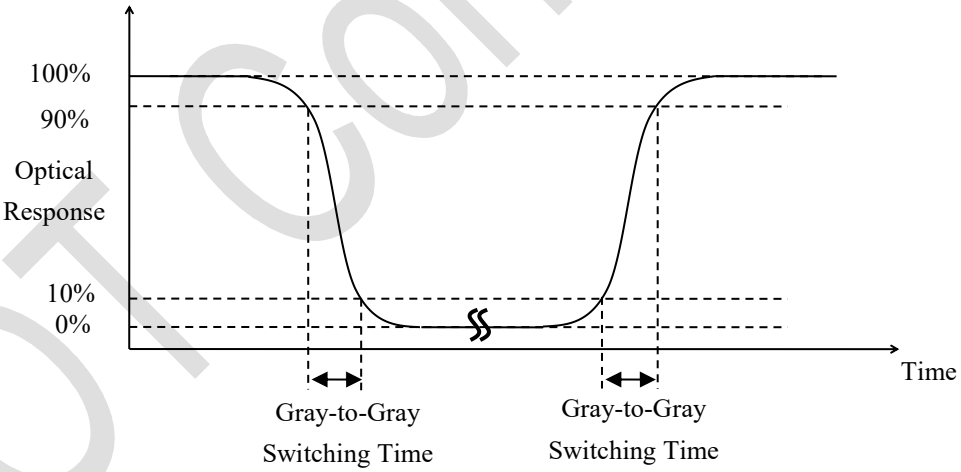


Note (4) Definition of Contrast Ratio (CR): The Contrast Ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = \frac{CR-W}{CR-D}$$

CR-W is the luminance at the center point of the LCD Module with full-screen displaying white. ; CR-D is the luminance at the center point of the LCD Module with full-screen displaying black.

Note (5) Definition of Gray-to-Gray Switching Time.



Gray to Gray average time means the average switching time of gray level 0, 31, 63, 95, 127, 159, 191, 223 and 255 to each other.

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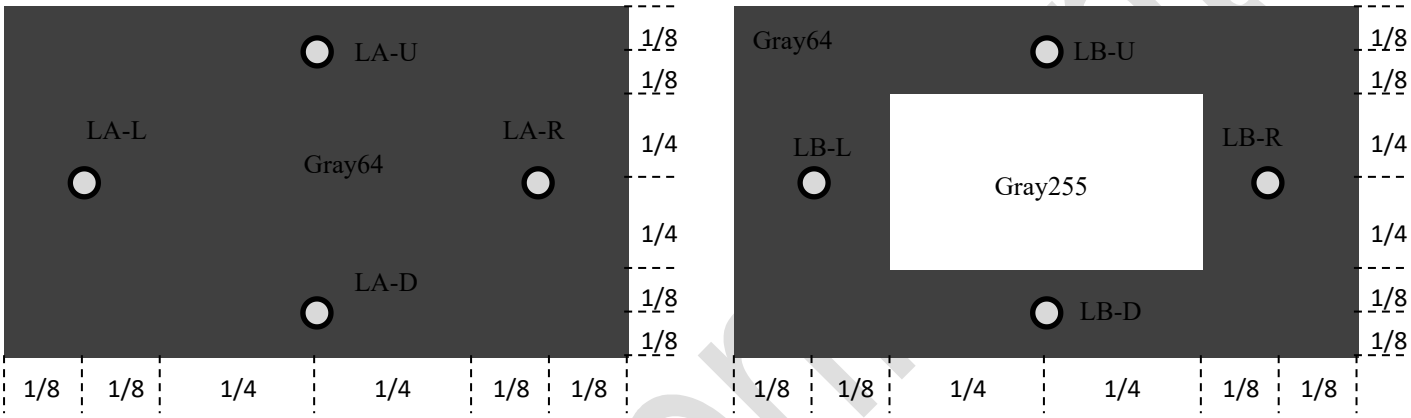
Note (6) Definition of center Transmittance (T %): The Transmittance is measured with full white pattern (Gray 255)

$$\text{Transmittance (T\%)} = \frac{\text{Luminance of white pattern}}{\text{Luminance of backlight}} \times 100\%$$

Note (7) Definition of Crosstalk(CT):

$$\text{Crosstalk (CT)} = \frac{|LB-LA|}{LA} \times 100\%$$

LA = Luminance of measured location without gray level 255 pattern (cd/m2)
LB = Luminance of measured location with gray level 255 pattern(cd/m2)



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7. Precautions

7.1 Assembly and Handling Precautions

(1) Improper or unbalanced forces are not used during assembly, such as bending or torsion.
(2) Do not apply pressure or impulse to an open cell to prevent the damage.
(3) It is recommended to assemble or to install an open cell into a customer's product in clean working areas. The dust and oil may cause electrical short to an open cell or worsen polarizers on an open cell.
(4) Always follow the correct power-on sequence when an open cell is assembled and turned on. This can prevent the damage and latch-up of the CMOS LSI chips.
(5) Do not design sharp-pointed structure / parting line / tooling gate on the plastic part of a COF (Chip on film), because the burr will scrape the COF.
(6) The bezel opening must have no burr and be smooth to prevent the surface of an open cell scraped.
(7) The gap between COF IC and any structure of BLU must be bigger than 2 mm. This can prevent the damage of COF IC.
(8) If COF would be bended in assemble process, do not place IC on the bending corner.
(9) The bezel of a module or a TV set cannot contact with force on the surface of an open cell. It might cause light leakage or scrape.
(10) In the case of no FFC or FPC attached with open cells, customers can refer the FFC / FPC drawing and buy them by self.
(11) It is important to keep enough clearance between customers' front bezel/backlight and an open cell. Without enough clearance, the unexpected force during module assembly procedure may damage an open cell.
(12) Do not plug in or unplug an I/F (interface) connector while an assembled open cell is in operation.
(13) Use a soft dry cloth without chemicals for cleaning, because the surface of the polarizer is very soft and easily scratched.
(14) Moisture can easily penetrate into an open cell and may cause the damage during operation.
(15) Unpacking (Cartons/Tray plates) in order to prevent open cells broken:
  (15-1) Moving tray plates by one operator may cause tray plates bent which may induce open cells broken. Two operators carry one carton with their two hands. Do not throw cartons/tray plates, avoid any impact on cartons/tray plates, and put down & pile cartons/tray plates gently.
  (15-2) A tray plate handled with unbalanced force may cause an open cell damaged. Trays should be completely put on a flat platform.
  (15-3) To prevent open cells broken, tray plates should be moved one by one from a plastic bag.
  (15-4) Please follow the packing design instruction, such as the maximum number of tray stacking to prevent the deformation of tray plates which may cause open cells broken.

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(15-5) To prevent an open cell broken or a COF damaged on a tray, please follow the instructions below:
- (15-5-1) Do not peel a polarizer protection film of an open cell off on a tray
- (15-5-2) Do not install FFC or V-by-One cables of an open cell on a tray
- (15-5-3) Do not press the surface of an open cell on a tray.
- (15-5-4) Do not pull X-board when an open cell placed on a tray.

(16) Unpacking (Hard Box) in order to prevent open cells broken:
- (16-1) Moving hard boxes by one operator may cause hard boxes fell down and open cells broken by abnormal methods. Two operators carry one hard box with their two hands. Do handle hard boxes carefully, such as avoiding impact, putting down, and piling up gently.
- (16-2) To prevent hard boxes sliding from carts and falling down, hard boxes should be placed on a surface with resistance.
- (16-3) To prevent an open cell broken or a COF damaged in a hard box, please follow the instructions below:
  - (16-3-1) Do not peel a polarizer protection film of an open cell off in a hard box.
  - (16-3-2) Do not install FFC or V-by-One cables of an open cell in a hard box.
  - (16-3-3) Do not press the surface of an open cell in a hard box.
  - (16-3-4) Do not pull X-board when an open cell placed in a hard box.

(17) When storing open cells as spares for a long time, the following precaution is necessary.
- (17-1) Do not leave open cells in high temperature and high humidity for a long time. It is highly recommended to store open cells in the temperature range from 0 to 35℃at normal humidity without condensation.
- (17-2) Open cells shall be stored in dark place. Do not store open cells in direct sunlight or fluorescent light environment.

(18) When ambient temperature is lower than 10ºC, the display quality might be reduced.

(19) Handling – In order to prevent open cells, COFs, and components damaged:
- (19-1) The forced displacement between open cells and X-board may cause a COF damaged. Use a fixture tool for handling an open cell to avoid X-board vibrating and interfering with other components on a PCBA & a COF.
- (19-2) To prevent open cells and COFs damaged by taking out from hard boxes, using vacuum jigs to takeout open cells horizontally is recommended.
- (19-3) Improper installation procedure may cause COFs of an open cell over bent which causes damages. As installing an open cell on a backlight or a test jig, place the bottom side of the open cell first on the backlight or the test jig and make sure no interference before fitting the open cell into the backlight/the test jig.
- (19-4) Handle open cells one by one.

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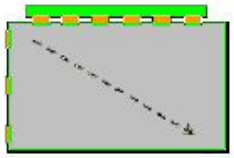
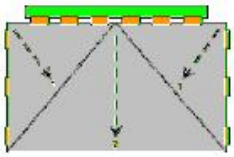
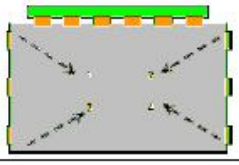
(20) The suggestion of removing polarizer-protection film is illustrated as following

(20-1) Scan COF on the left side (Figure 1) Remove slowly and follow the direction：from left-up to right-down

(20-2) Scan COF on the right side (Figure 2) Remove slowly and follow the direction: from right-up to left-down

(20-3) Scan COF on the left and right side (Figure 3) Remove slowly and follow the direction as marked by 1 and 2.

(20-4) Scan COF on the left and right side (Figure 4) Remove slowly and follow the direction as marked by 1, 2, 3 and 4.

Figure 1	Figure 2	Figure 3	Figure 4
			

(21) Avoid any metal or conductive material to contact PCB components, because it could cause electrical damage or defect.

7.2 Safety Precautions

(1) If the liquid crystal is leaked from open cell, be sure to keep your eyes and mouth away. When touching hands, skin or clothing, you must wash thoroughly with soap.

(2) After the end of life, open cells are not harmful in case of normal operation and storage.

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8. Definition of Labels

8.1 Open Cell Label

Label Size：20mm（L）*10mm（W）



Serial ID Naming Rule：

Serial ID Rule												
1	2	3	4	5	6	7	8	9	10	11	12	13
FAB vender	Model in code		Revision		Year	Month	Day	CHOT Internal Use				

8.2 BOX Label

Label Size: 90mm（L）*55mm（W）



BOX ID Naming Rule：

BOX ID Rule											
1	2	3	4	5	6	7	8	9	10	11	12
FAB vender	Model in code		Revision		Year	Month	Day	CHOT Internal Use			

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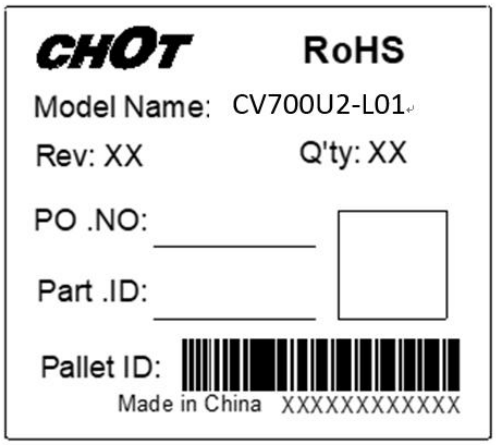
8.3 Pallet Label

Label Size: 90mm（L）*80mm（W）



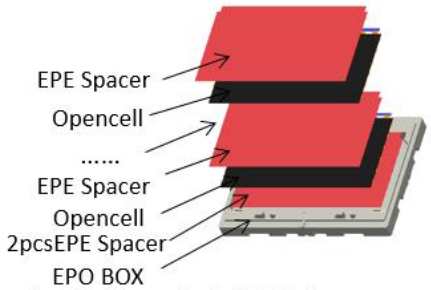
Pallet ID Naming Rule：

Pallet ID Rule								
1	2	3	4	5	6	7	8	9
FAB vender	Year	Month	Day	CHOT internal use				

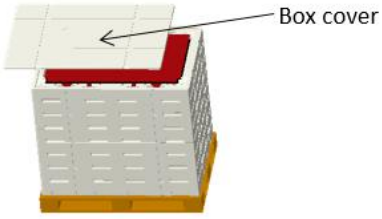
9. Packaging

9.1 Packaging Flow

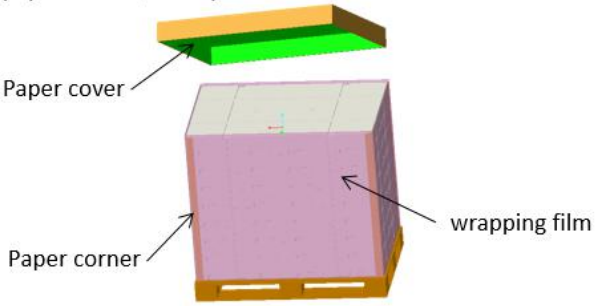
1、Put EPE Spacer and Opencells into the box，Per box has 9pcs Opencells and 11pcs EPE Spacers.



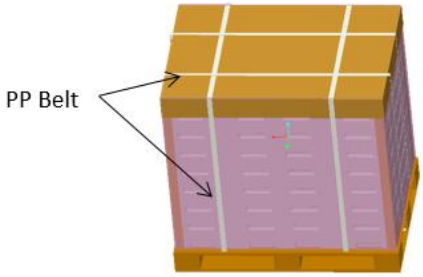
2、Put 8ea boxes on the Pallet；Then put 1ea box cover on the top of the boxes.



3、 Put paper corner 、use wrapping film to bind up them，Then paper cover（waterproof） on the box cover



4、Finally, pack with PP Belt



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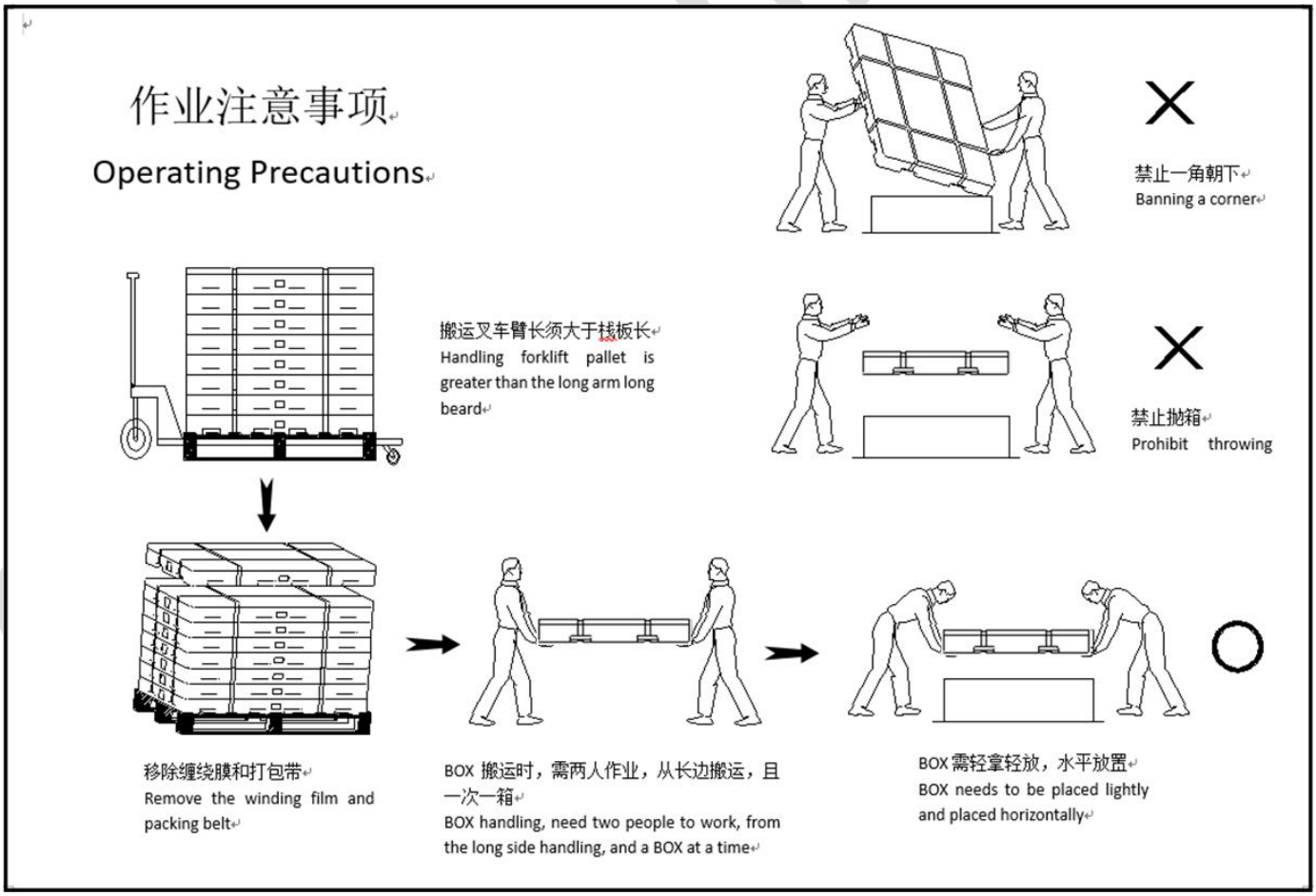
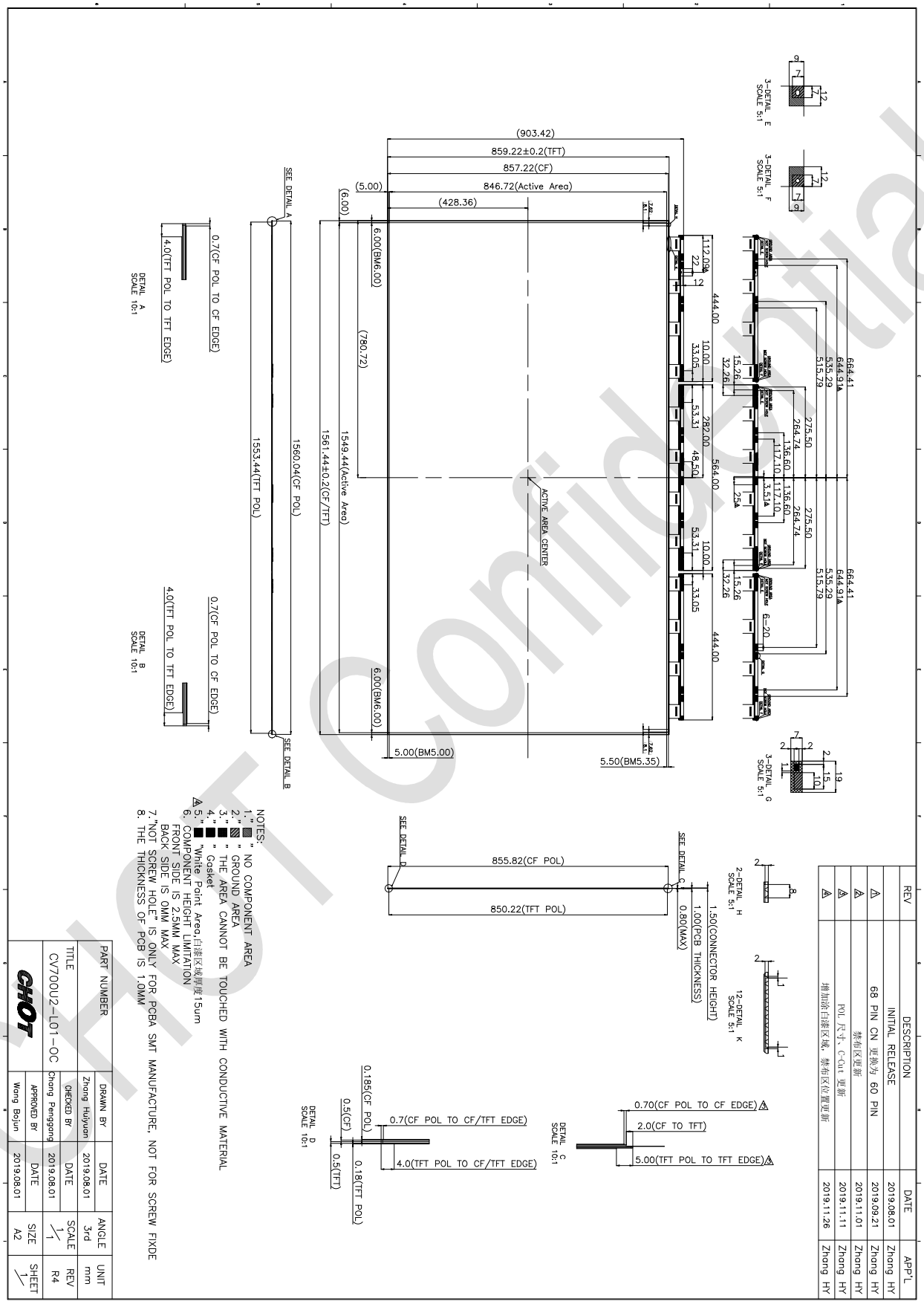
9.2 Packaging Note

Item	Material	Specification		
		Size(mm)	Qty.	Weight（Kg）/Typ.
1	BOX	1846*1112*123.5	9pcs Open-cells/BOX	（44.29）
2	BOX Cover	1846*1112*40	1ea	（3.68）
3	Pallet	1866*1132*148	1ea	35.60
4	Boxes Per Pallet	8box/Pallet		
5	Open-cells Per Pallet	72pcs Open-cells/Pallet		
6	Pallet after Packing	1866*1132*1106	72pcs	（398.93）

9.3 Packaging Precautions



10. Mechanical Characteristic



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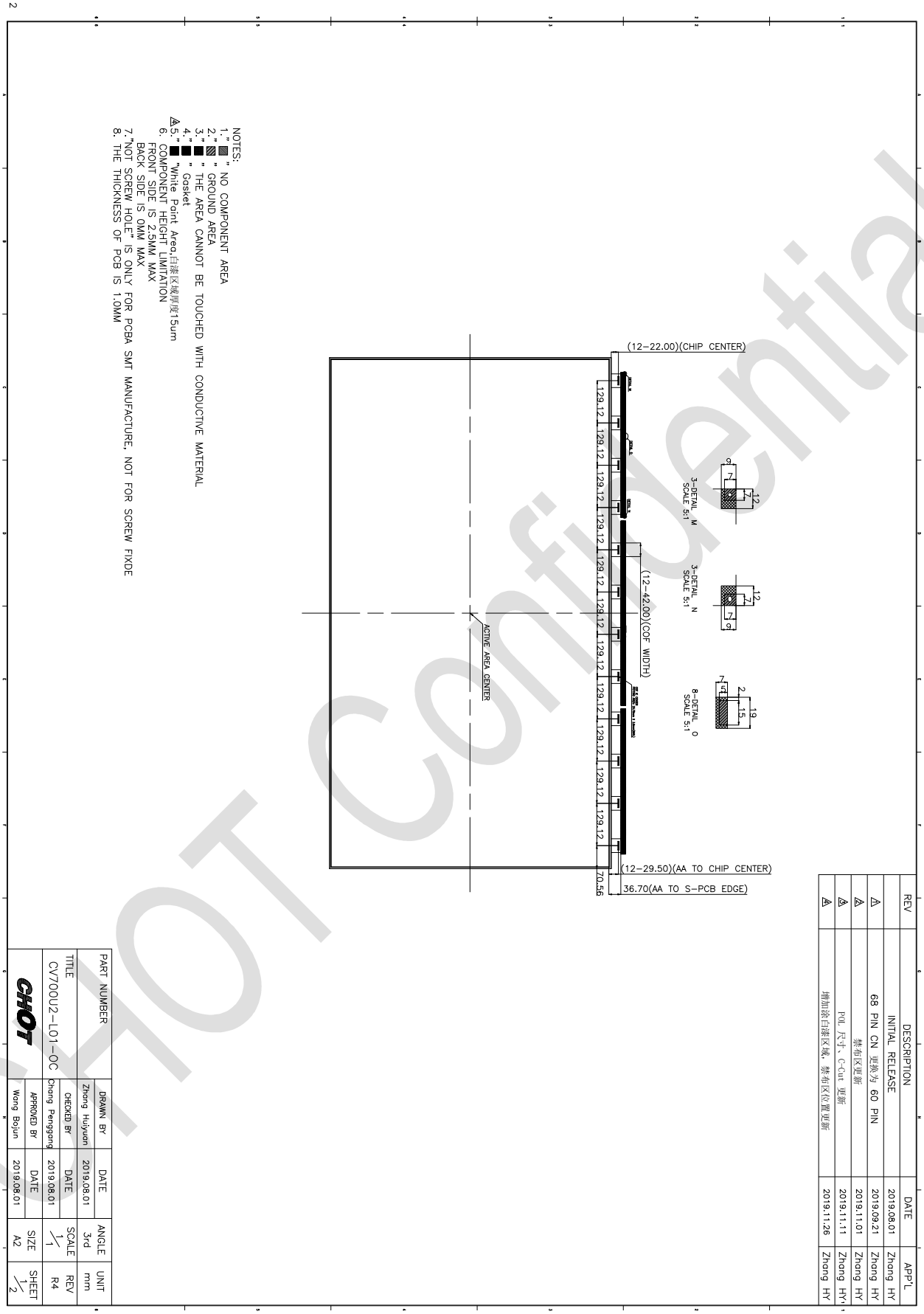

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